

FIG. 1

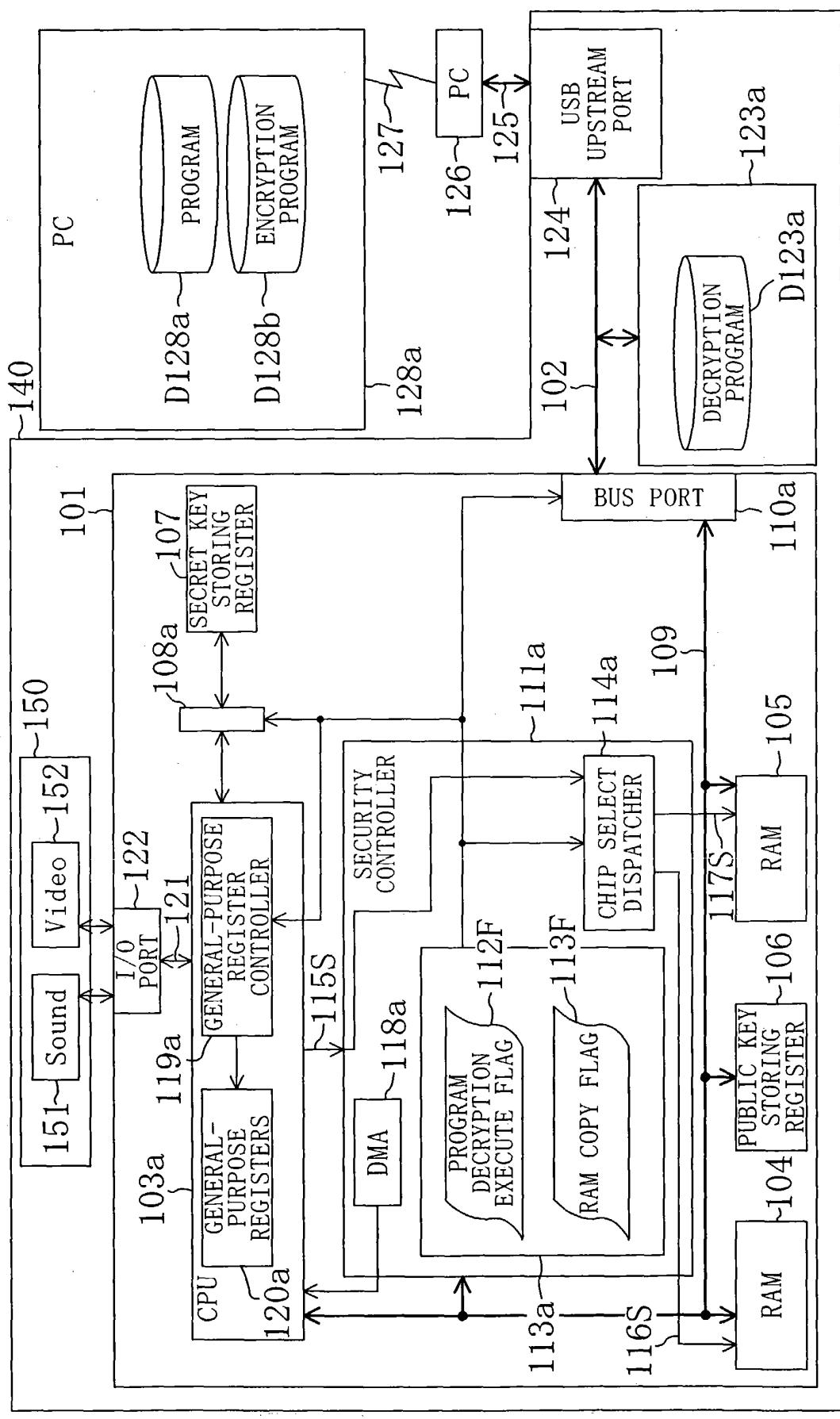


FIG. 2

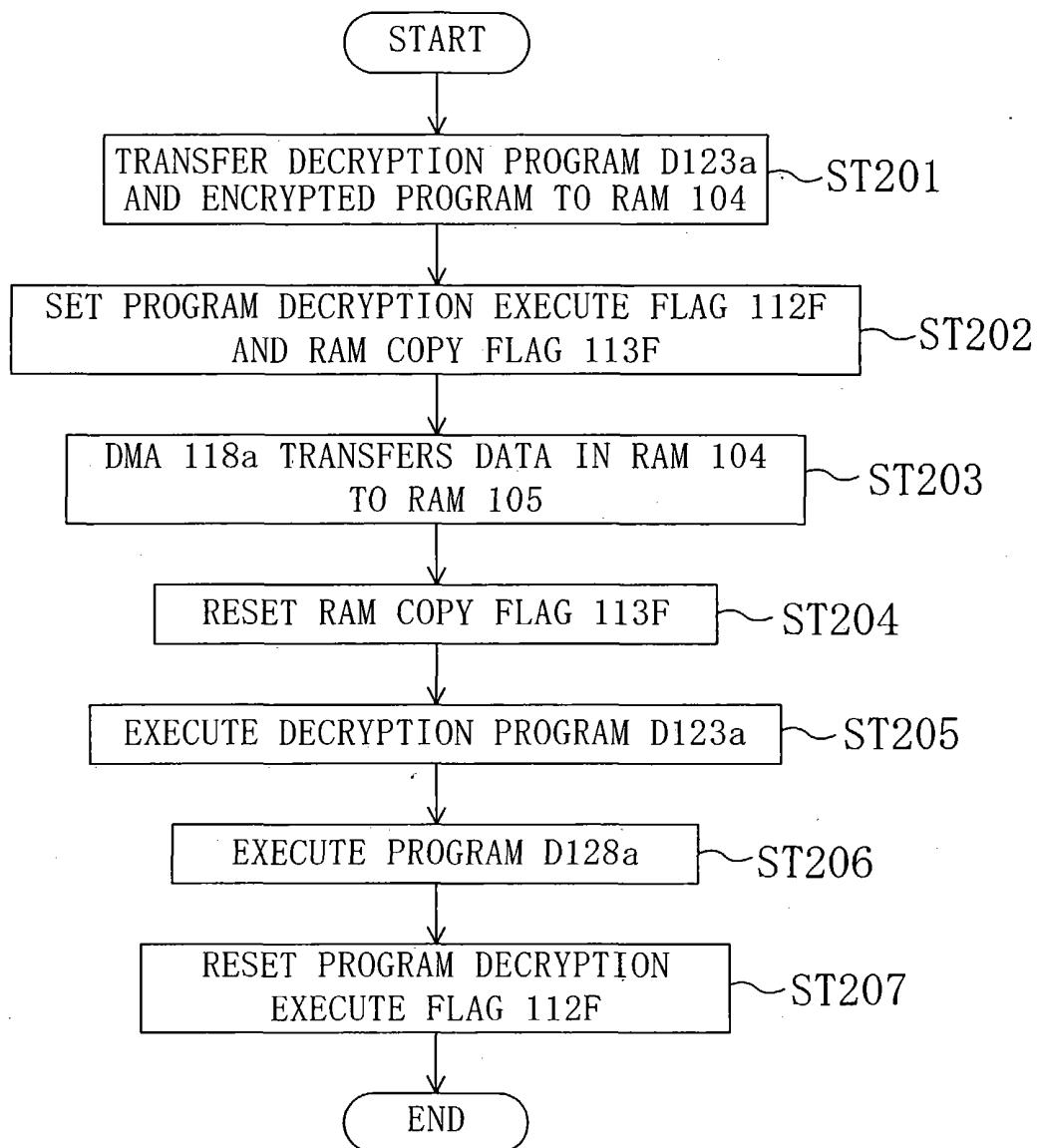


FIG. 3

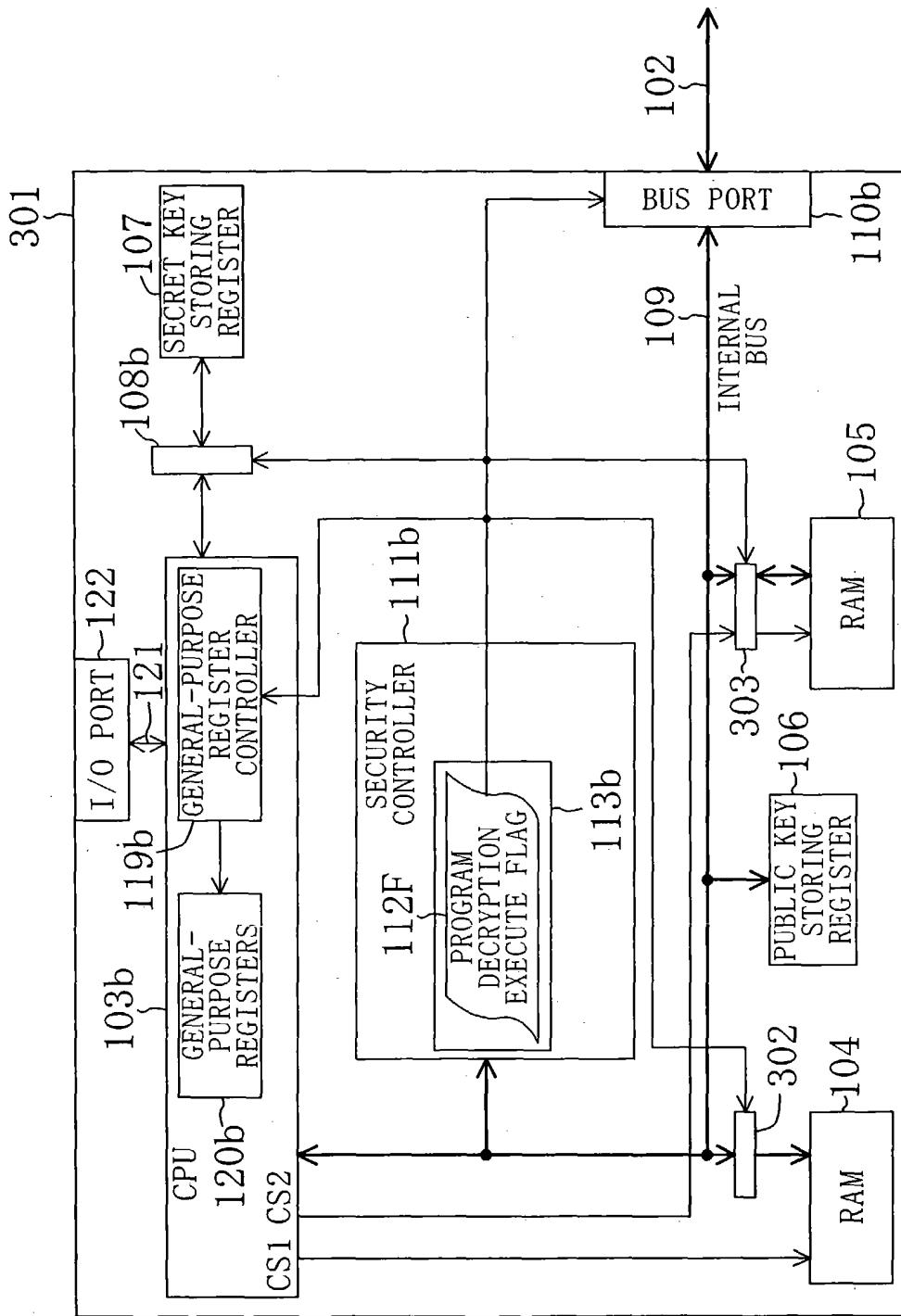


FIG. 4

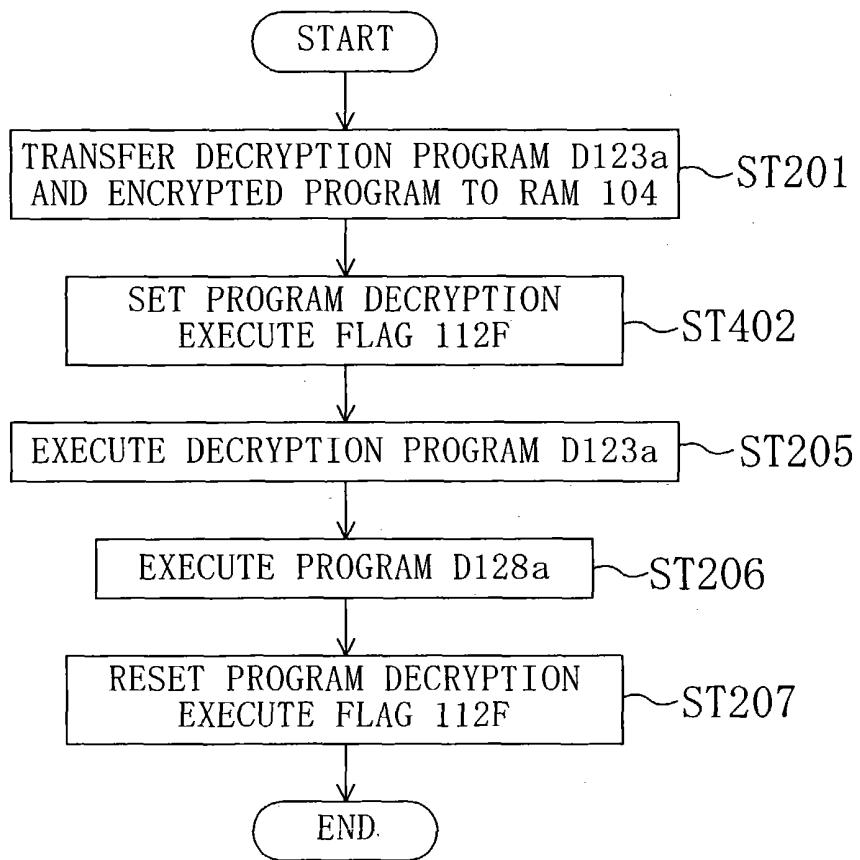


FIG. 5

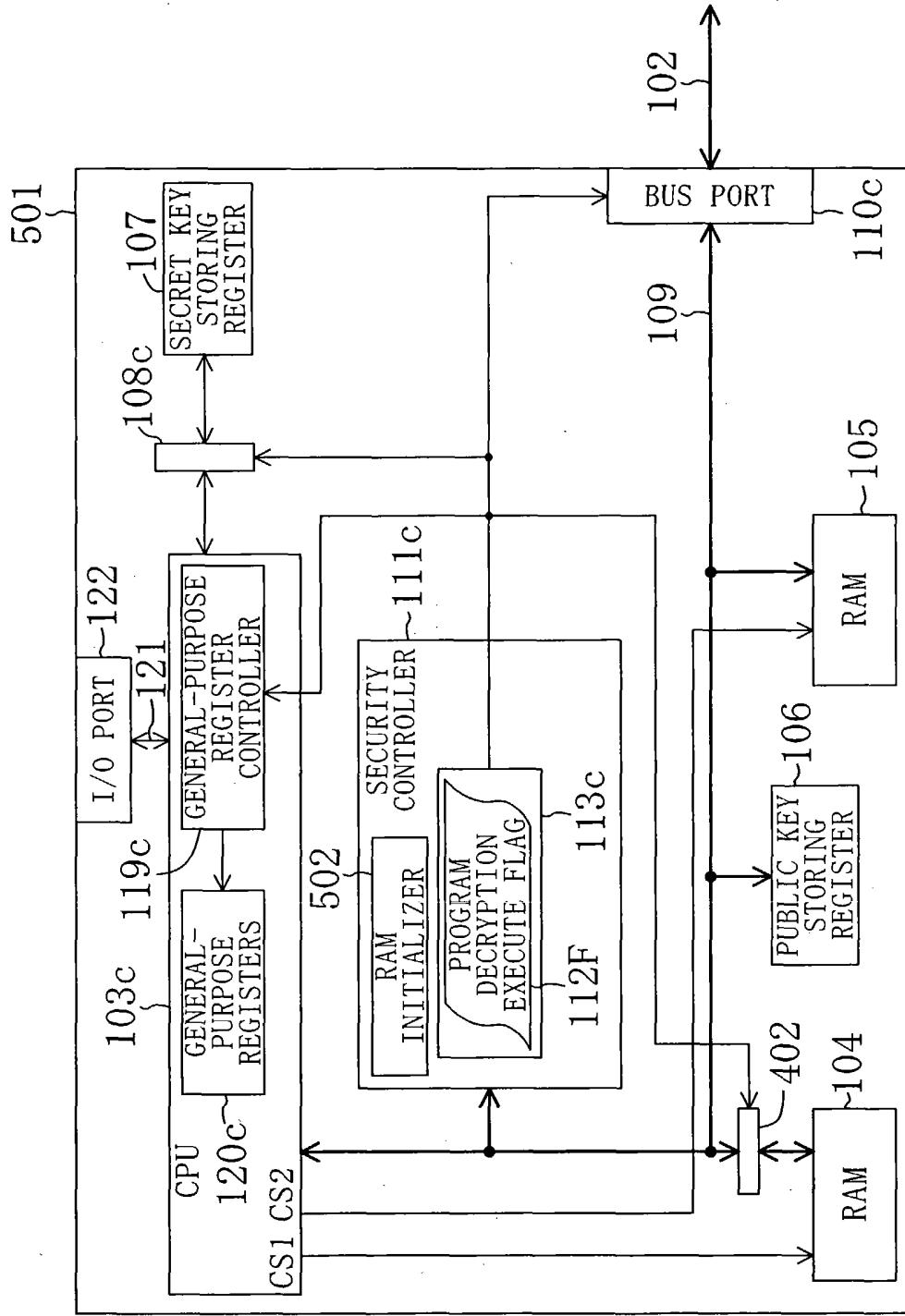


FIG. 6

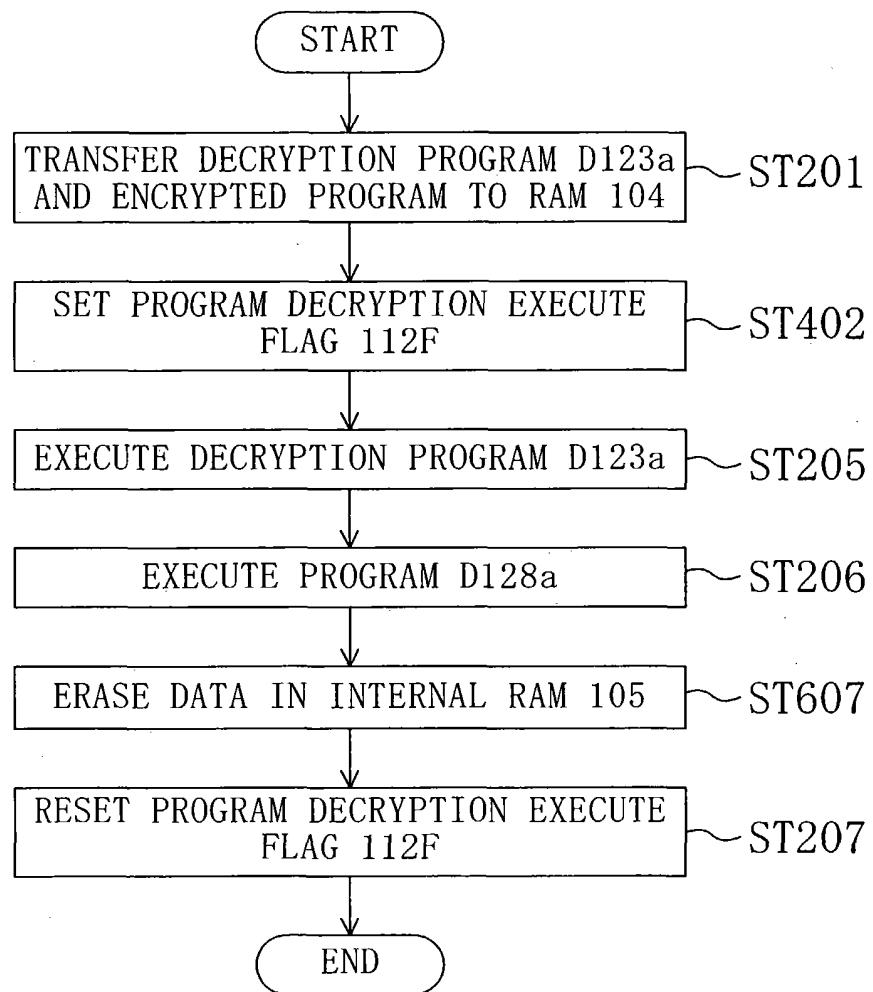


FIG. 7

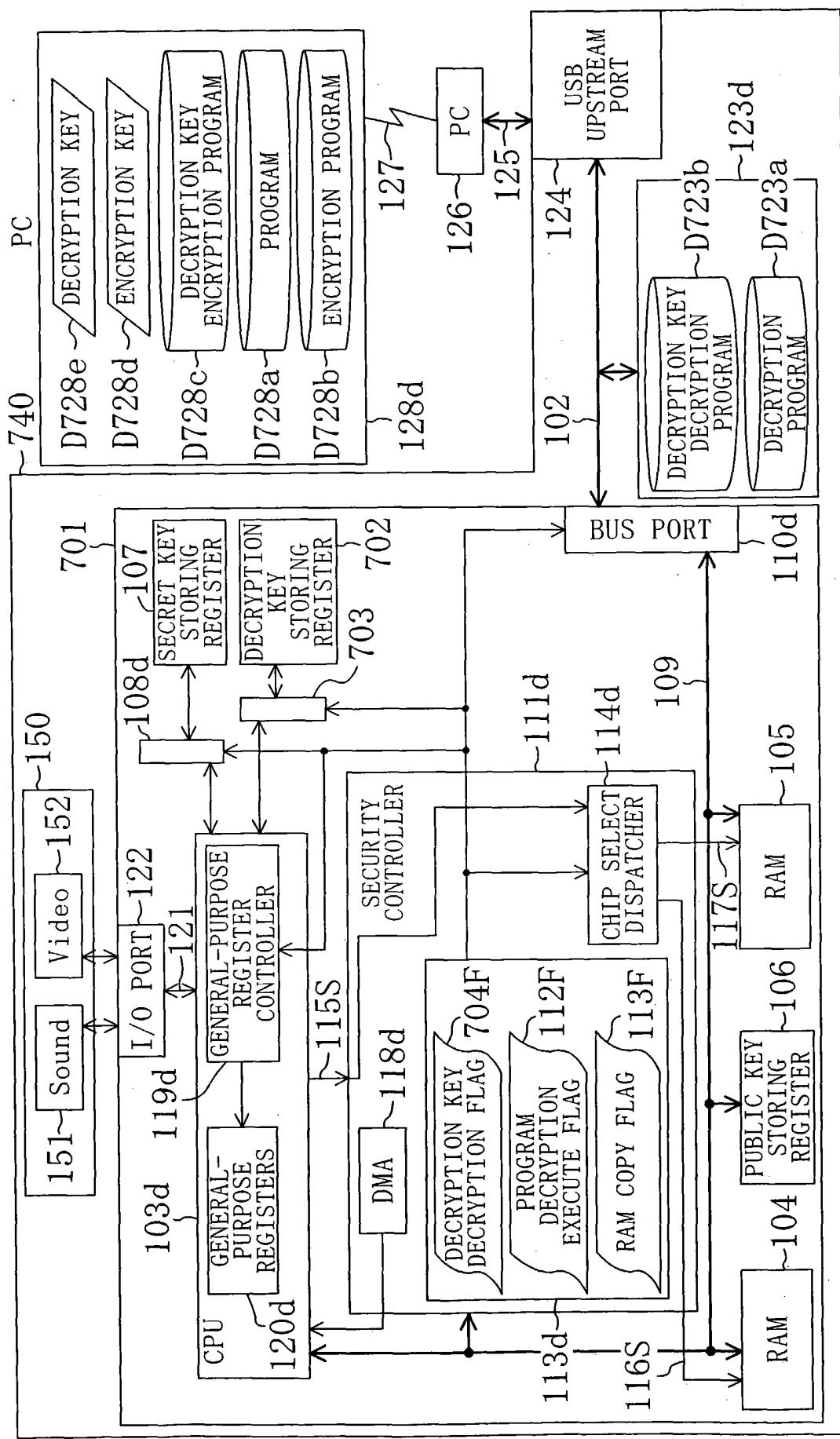


FIG. 8

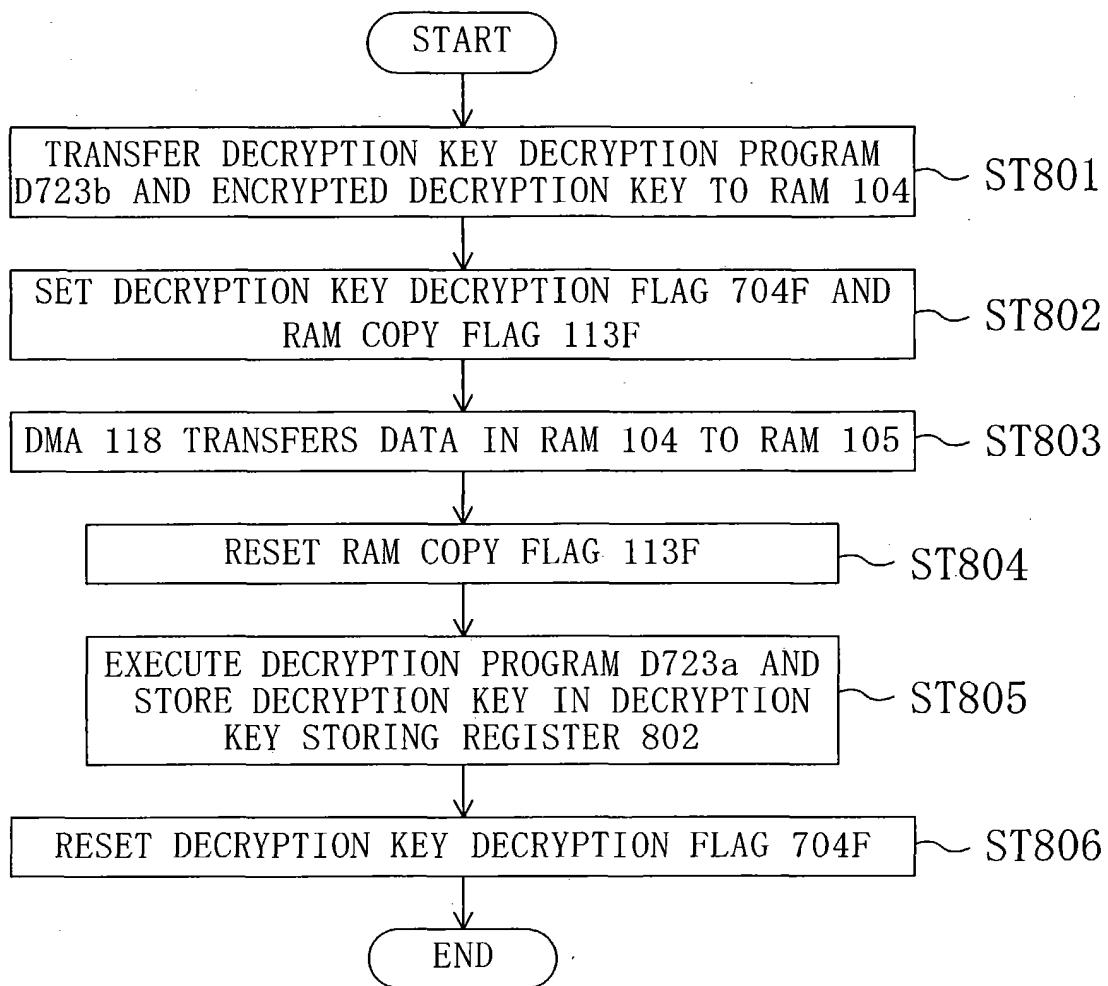


FIG. 9

RAM COPY FLAG 113F	0	1	0	1	0
DECRYPTION KEY DECRYPTION FLAG 704F	0	1	1	0	0
PROGRAM DECRYPTION EXECUTE FLAG 112F	0	0	0	1	1
BUS PORT 110d	OPEN	CLOSE	CLOSE	CLOSE	CLOSE
SECRET KEY ACCESS PORT 108d	CLOSE	CLOSE	OPEN	CLOSE	CLOSE
DECRYPTION KEY ACCESS PORT 703	CLOSE	CLOSE	OPEN	CLOSE	OPEN
CHIP SELECT SIGNAL 116S	CS115	ASSERT	NEGATE	ASSERT	NEGATE
CHIP SELECT SIGNAL 117S	NEGATE	ASSERT	CS115	ASSERT	CS115

FIG. 10

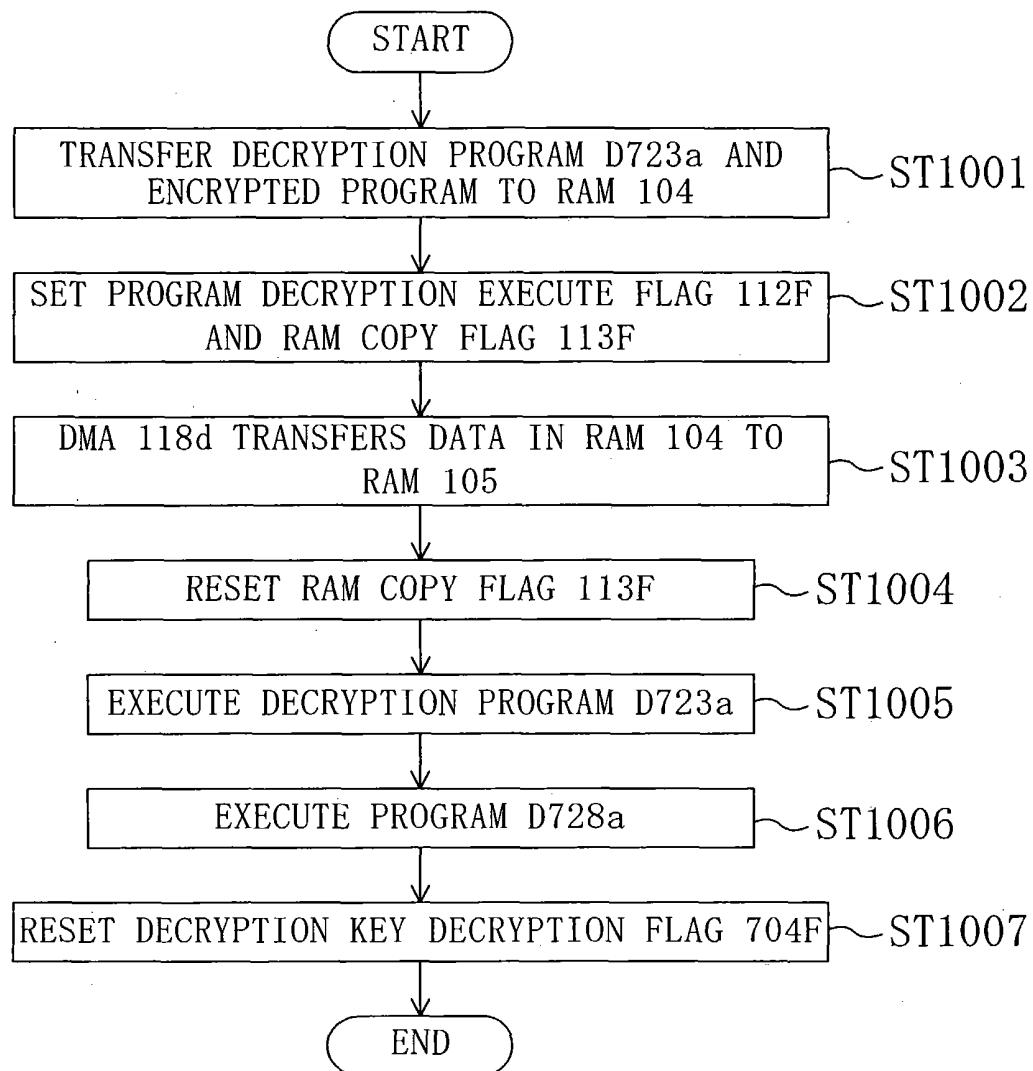


FIG. 11

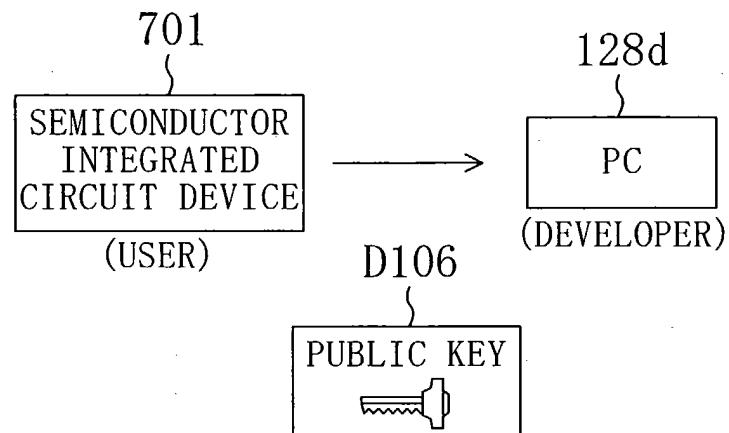


FIG. 12

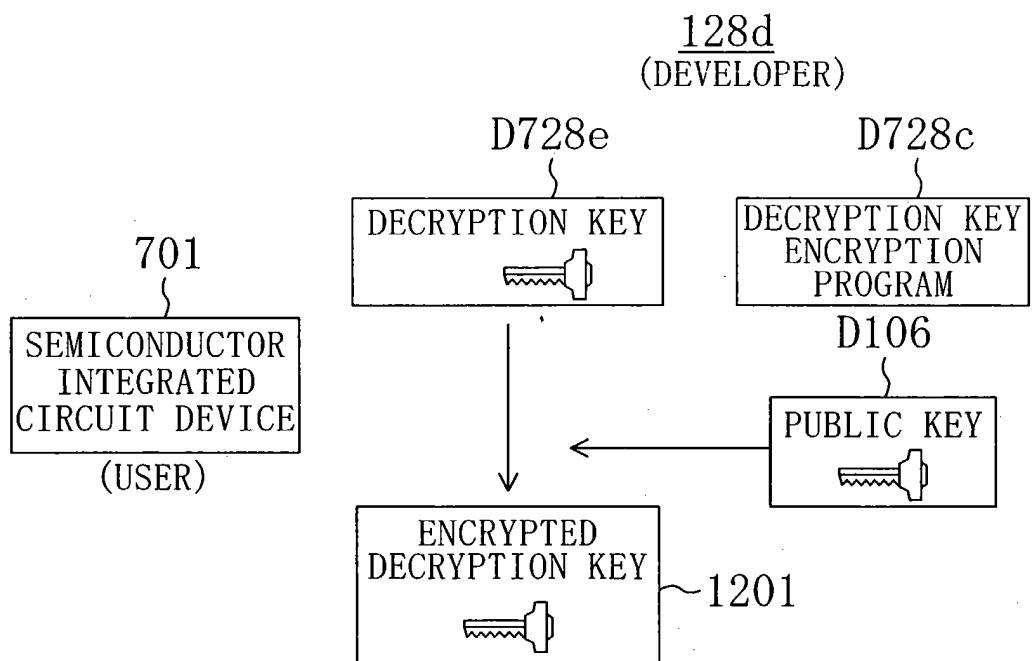


FIG. 13

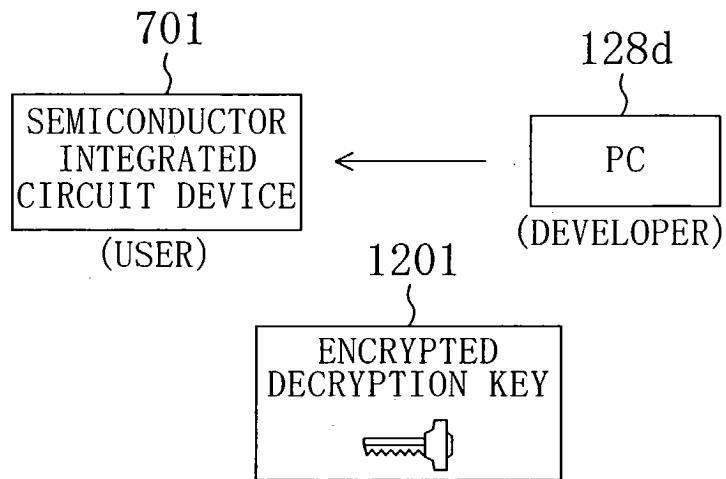


FIG. 14

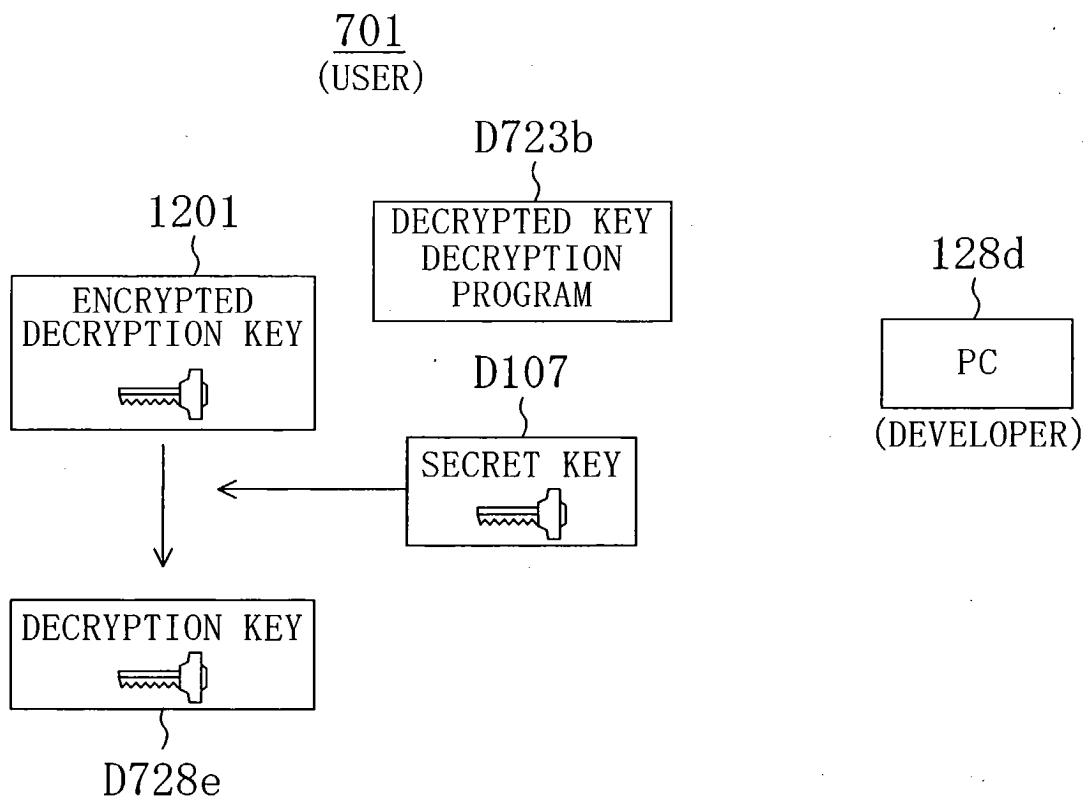


FIG. 15

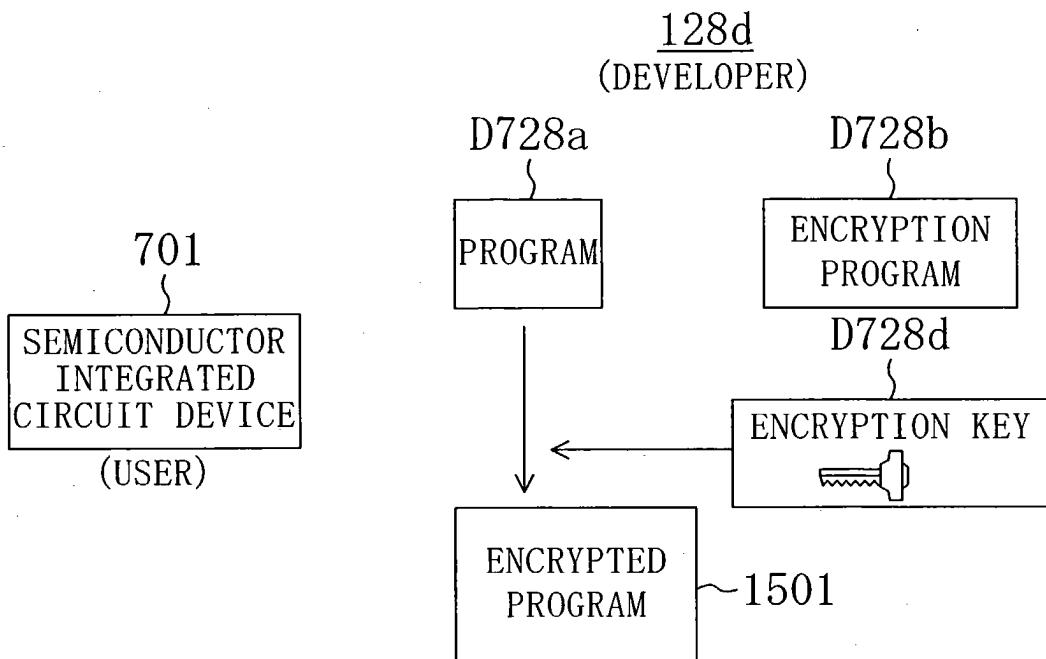


FIG. 16

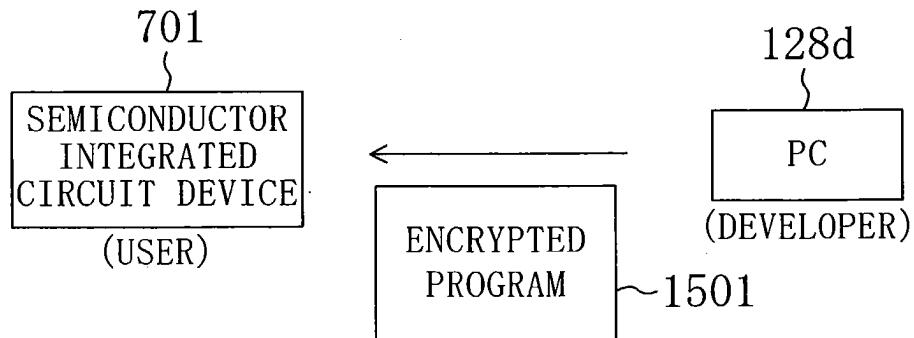


FIG. 17

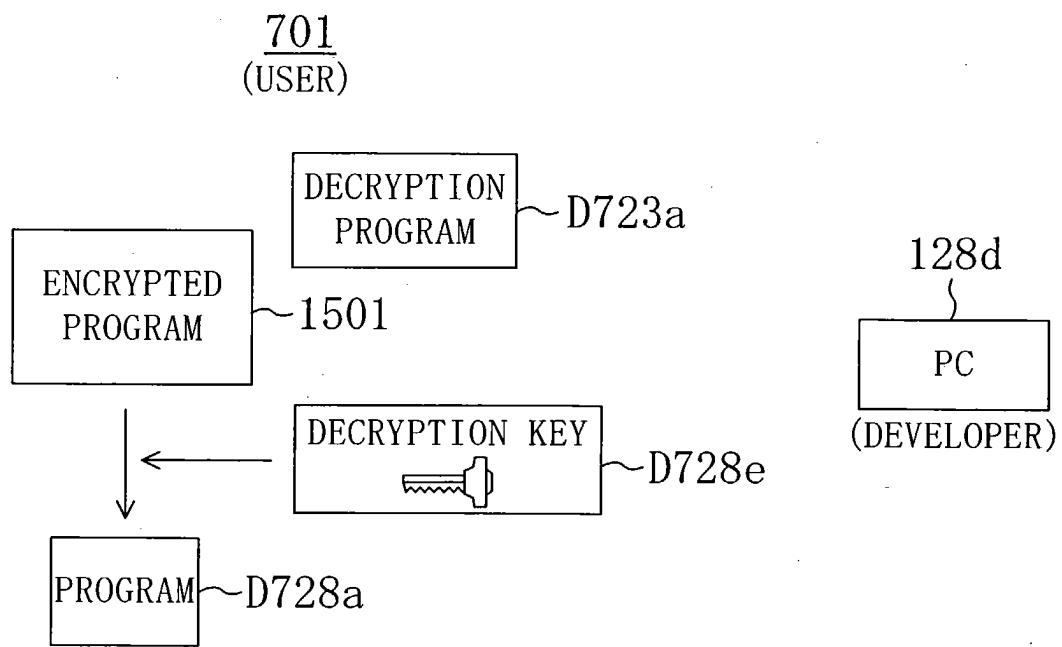


FIG. 18
PRIOR ART

